```
RRR
RRR
RRR
RRR
RRR
                                   FFF
FFF
FFF
FFF
FFF
                 RRR
RRR
RRR
                              RRR
RRR
RRR
```

Va

	MM MMM MMMM MMMM MMMM MMMM MMMM MMM MM MM	88888888 88 88 88 88 88 88 88 88 88 88 888888	TTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTTT	DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDD	
RRRRRRRR RRRRRRRR RR RR RR RR RR RR RRRRRR		QQQQQQ QQ QQ QQ QQ QQ QQ QQ QQ QQ QQ QQ QQ QQ			

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RE

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!*** MODULE EMBET ***

ERROR MESSAGE ENTRY TYPE DEFINITIONS

```
Literal EMBSC_DE = 1;
Literal EMBSC_DE = 1;
Literal EMBSC_MC = 2;
Literal EMBSC_MC = 2;
Literal EMBSC_MC = 2;
Literal EMBSC_BE = 4;
Literal EMBSC_BE = 4;
Literal EMBSC_SA = 5;
Literal EMBSC_SA = 6;
Literal EMBSC_SA = 6;
Literal EMBSC_AW = 7;
Literal EMBSC_AW = 7;
Literal EMBSC_AW = 7;
Literal EMBSC_AW = 7;
Literal EMBSC_AW = 8;
Literal EMBSC_AW = 8;
Literal EMBSC_HE = 8;
Literal EMBSC_UBA = 9;
Literal EMBSC_UBA = 9;
Literal EMBSC_UBA = 9;
Literal EMBSC_SI = 10;
Literal EMBSC_SI = 10;
Literal EMBSC_SI = 10;
Literal EMBSC_UE = 11;
Literal EMBSC_UE = 11;
Literal EMBSC_UE = 11;
Literal EMBSC_UE = 11;
Literal EMBSC_MBA = 12;
Literal EMBSC_SBIA = 13;
Literal EMBSC_SBIA = 13;
Literal EMBSC_CRD = 14;
Literal
```

DE

```
11/790 Environmental Monitor
11/790 Environmental Monitor
11/790 Processor Error Halt
11/790 Processor Error Halt
11/790 Processor Error Halt
11/790 Console Reboot
COLD START (IE: SYSTEM BOOT)
COLD START (IE: SYSTEM BOOT)
NEW FILE CREATED
WARM START (IE: SYSTEM POWER RECOVERY)
WARM START (IE: SYSTEM POWER RECOVERY)
WARM START (IE: SYSTEM POWER RECOVERY)
CRASH RE-START
TIME STAMP ENTRY
TIME STAMP ENTRY
SYSTEM SERVICE MESSAGE
SYSTEM SERVICE MESSAGE
SYSTEM BUGCHECK
OPERATOR MESSAGE
NETWORK MESSAGE
NETWORK MESSAGE
NETWORK MESSAGE
NETWORK MESSAGE
VOLUME MOUNT
VOLUME DISMOUNT
VOLUME DISMOUNT
VOLUME DISMOUNT
UNDEFINED INTERRUPT
UNDEFINED INTERRU
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           16-SEP-1984 16:50:35.45 Page 2
                 EMBETDEF.REQ;1
literal EMBSC_EMM = 15;
literal EMBSK_EMM = 15;
literal EMBSK_HLT = 16;
literal EMBSK_HLT = 16;
literal EMBSK_CRBT = 17;
literal EMBSK_CRBT = 17;
literal EMBSK_CRBT = 17;
literal EMBSK_CRBT = 35;
literal EMBSK_CS = 32;
literal EMBSK_NF = 35;
literal EMBSK_WS = 36;
literal EMBSK_WS = 36;
literal EMBSK_CR = 37;
literal EMBSK_CR = 37;
literal EMBSK_TS = 38;
literal EMBSK_TS = 38;
literal EMBSK_TS = 38;
literal EMBSK_TS = 39;
literal EMBSK_SBC = 40;
literal EMBSK_SBC = 40;
literal EMBSK_OM = 41;
literal EMBSK_OM = 42;
literal EMBSK_OM = 42;
literal EMBSK_OM = 42;
literal EMBSK_OM = 64;
literal EMBSK_OM = 98;
literal EMBSK_OD = 96;
literal EMBSK_OD = 96;
literal EMBSK_OD = 96;
literal EMBSK_OD = 96;
literal EMBSK_OD = 98;
literal EMBSK_OD = 100;
literal EMBSK_OD = 101;
literal EMBSK_OD = 112;
```

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